



TXW828
QFN68/80/96

TXW828

High Performance ISP+Audio/Video+Wi-Fi/BLE SOC

The TXW828 is a low-power, high-performance, highly integrated audio and video wireless SoC chip. It integrates 2.4GHz Wi-Fi, BLE, high-performance ISP image processing, and H.264/MJPEG codec internally, offering excellent RF performance, superior image processing capabilities, encoding quality, and relatively low bit rates. It also includes an internal security algorithm acceleration engine and features a complete set of peripheral interfaces: audio ADC/DAC, MIPI CSI, MIPI DSI, DVP, LCD, RMI MAC, USB 2.0 High Speed Host/Device, SDMMC Host, I2S, PDM, and many other rich peripheral interfaces.

The TXW828 supports running programs from SPI Flash, supports RTOS and third-party components, and comes with an open and user-friendly development and debugging environment.

The TXW828 is available in QFN68/80/96 package types. Depending on the specific package, the peripheral resources within the device may vary; some packages support built-in PSRAM and Flash.

TXW828 Wi-Fi SOC

- Dual core 240MHz
- 32KB I-cache, 32KB D-cache
- 368KB SRAM(560KB MAX)
- Built-in 2MB Flash
- Built-in 4MB PSRAM
- SPI Flash firmware encryption protection
- H.264/MJPEG Codec(1080P@30fps)
- USB 2.0 Host/Device
- 2.4GHz Wi-Fi (150Mbps MAX)
- Wi-Fi/BLE co-existence
- MIPI CSI-2(1920*1080@30/fps)
- MIPI DSI with 2/4 lanes
- Image Signal Processor(ISP)

Applications

- FPV devices
- IPC(multi-cameras)
- Baby monitor
- Intercom
- Dash Camera(multi-channels)
- LCD Displays



For more information please visit: <https://www.txw.ac/chips/txw828>